Evaluation study and comparison between four techniques of incremental conductance MPPT method for photovoltaic system

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Abstract— In recent years, several researchers have presented various algorithms to track the maximum power of photovoltaic module. The Incremental Conductance (IC) method is widely used because it is less complicated and has good tracking accuracy. This paper presents an evaluation study and comparison between four techniques of the Incremental Conductance Method, namely: Incremental Conductance with fixed step size (FS_IC), Incremental Conductance with variable step size (VS_IC), Incremental Conductance with first proposition of modified variable step size (MVS1_IC) and Incremental Conductance with second proposition of modified variable step size (MVS2_IC), these four techniques are simulated by Psim software. Results show the good tracking efficiency of the MVS1_IC and MVS2_IC techniques compared to FS_IC and VS_IC, also the MVS2_IC MPPT algorithm is the most efficient and presented less energy loss.

Keywords— Incremental Conductance, MPPT, Photovoltaic (PV),

I. INTRODUCTION

Photovoltaic solar energy comes from the direct transformation of part of the solar radiation into electrical energy. This conversion of energy takes place via a photovoltaic cell. The association of several PV cells in series / parallel gives rise to a photovoltaic generator (PVG) which has a not-linear current-voltage characteristic (I-V) and has a maximum power point (MPP) [1],[2].

The transfer of the maximum power of the photovoltaic generator (GPV) to the load often suffers from poor adaptation. The literature proposes a large quantity of solutions on the control algorithm which performs a maximum power point track when the GPV is coupled to a load through a static converter [3].

Incremental inductance method present one of the best controller of DC-DC converter, it is less complicated and has good tracking accuracy [4]. In recent years, various MPPT techniques of incremental conductance have been suggested, namely: the fixed step size incremental conductance technique (FS_IC) [5] and the variable step size incremental conductance technique (VS IC). The first one used a fixed step size to

follow the MPP, this later is reached when the slope of P-V curves is zero. Thus, the accuracy and speed of the response time are highly dependent on the defined step size: if the step size is low, the accuracy is high and the speed response is so slow, but situation is reversed with a larger step size.

To overcome this problem, a second technique VS_IC with direct control is proposed [6],[7], but it presents some defaults corrected by others techniques named modified variable step size incremental conductance technique (MVS_IV).

In this article, we present a study of the performance of IC algorithms with fixed and variable step size for photovoltaic systems, the influence of the two techniques on the behavior of the system and the various advantages and disadvantages are highlighted. Also a modified VS_IC techniques that correct different drawbacks of FS_IC and VS_IC are presented.

II. PHOTOVOLTAIC SYSTEM

A photovoltaic system consists of three parts as shown in Fig. 1. The first one represents the photovoltaic generator, the second part is a DC-DC converter controlled by MPPT controller. The third part represents the load [1].

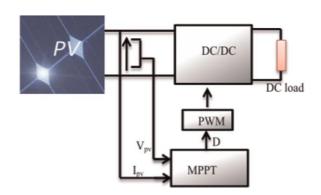


Fig. 1 Photovoltaic system block diagram.

A. Pv Generator

A physical model of Solarex MSX-60 PV pannel proposed by Psim software is represented in Fig. 2. The electrical caracteristics of this pannel is shown in Table I.

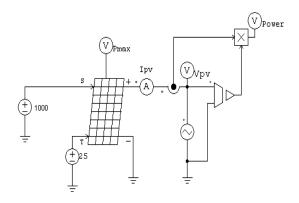


Fig. 2 Photovoltaic model of PVG in Psim.

TABLE I ELECTRICAL CHARACTERISTICS OF OF SOLAREX MSX-60 (1kW/m², 25 °C)

Description	MSX-60
Maximum power (Pm)	60W
Voltage Pmax (Vmpp)	17.1V
Current at Pmax (Impp)	3.5A
Short circuit curren t(Isc)	3.8A
Open circuit voltage (Voc)	21.1
Temperature coeff.of Voc	- (80±10)mV/°C
Temperature coeff.of Isc	(0.065±0.01)%°C
Temperature coeff.of power	(-0.5±0.05)% °C
Nominal operating cell temperature NOCT2	47±2 °C

B. DC-DC buck converter

In order to extract the maximum power from the PV module, it is necessary to adapt the PV panel to the load. This adaptation is carried out by means of the DC-DC converter [3],[8].

The model that we have chosen in our study is a buck converter shown in Fig. 3.

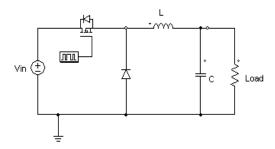


Fig. 3 DC-DC buck converter circuit.

C. Incremental conductance MPPT method

In general, the conventional algorithm IC MPPT uses a fixed step to follow the maximum power point, in this case it is called fixed step size incremental conductance MPPT technique (FS_IC), this technique presents drawbacks which are corrected by the development of another technique with a variable step size named variable step size

incremental conductance MPPT technique (VS_IC). Fig. 4 illustrates a manner of tracking the MPP using these two steps of incrementing [4],[9].

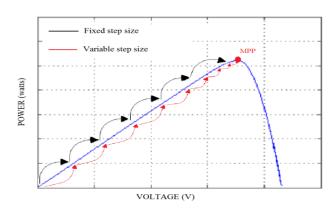


Fig. 4 Fixed and variable step size IC MPPT method.

1) Incremental conductance method with fixed step size (FS_IC)

The algorithm of this method measures at first the voltage V and the current I from the photovoltaic generator (PVG), the second state is to calculate the output power P and its derivative as a function of the voltage dP/dV. The third state is to use the derivative of the power-voltage output characteristic to decide whether the duty cycle should be increased or decreased [4],[5]. The flow chart of the FS_IC algorithm is shown in Fig. 5.

The output power and it's derivative is given by following equations:

$$P = V . I \tag{1}$$

$$dP = V \cdot dI + I \cdot dV \tag{2}$$

The dP/dV ratio can be expressed as:

$$dP/dV = I/V + dI/dV (3)$$

The dP/dV is defined as the maximum power point identification factor used for tracking the MPP. The following equations are considered to track the MPP [4],[5].

$$dI/dV = -I/V \text{ at MPP}, \tag{4}$$

$$dI/dV > -I/V$$
 at left of MPP (5)

$$dI/dV < -I/V$$
 at right of MPP (6)

The disadvantage of IC method with a fixed increment is that if we use a large increment, the MPP search is faster, but it

causes excessive oscillations around the PPM, which produces a low yield [9]. This situation is reversed when the increment step is small. To solve this problem, several IC algorithms of variable step size are presented in the literature [10],[11].

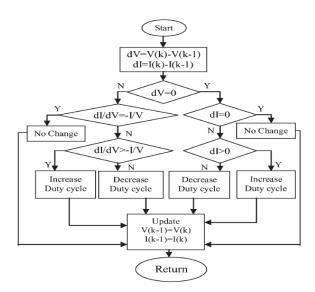


Fig. 5 Flow chart of IC method with fixed step size.

2) Incremental conductance method with variable step size (VS_IC)

Different IC MPPT algorithms with variable increment steps are proposed in the literature. The principle of this algorithm is that if the operating point is far from MPP, it increases the increment step size which allows a fast tracking of the MPP whereas if the operating point is near to the MPP, the step size becomes very small so the oscillation becomes very reduced contributing to a higher efficiency [12],[13].

The variable step size adopted to reduce this problem is represented as follow [14]:

$$Step = N \left| \frac{dP}{dV} \right| \tag{7}$$

Where:

N is the scale factor that is set during the design to adjust the step size. To increase the convergence of this algorithm the variable step size must satisfy the following inequality:

$$N\left|\frac{dP}{dV}\right| < \Delta D_{max} \tag{8}$$

Where:

Δ*Dmax* is the largest step size for FS_IC MPPT. The scaling factor can be obtained as:

$$N < \Delta D_{max} / \left| \frac{dP}{dV} \right| \tag{9}$$

When equation 9 cannot be satisfied, the increment step takes the maximum value of the fixed step ΔD max previously set. This method can increase the speed of convergence and also reduce oscillations in steady state [5],[14].

The flow chart of the VS_IC algorithm is shown in Fig. 6.

According to equation 8 the dP/dV is all time compared to a constant ($\Delta Dmax/N$ is constant).

As shown in Figure 7, curve P1 and P2 are the output power of a PV array under different irradiation levels. The scaling factor N1 and upper limiter step size $\Delta Dmax1$ are chosen by reference to P1; in this case, fast dynamic response and good steady performance are achieved simultaneously. However, when irradiation changes greatly, the same parameters always make the system operate within the variable step size mode for P2 curve, which increases the start-up time, as well as the response time. If the scaling factor N2 and upper limiter of step size $\Delta Dmax2$ are selected according to power curve P2, the variable step size area of the system that worked for P1 curve becomes too small, which incurs severe oscillations at steady state and continuous power loss. All in all, the parameters have a significant effect on the system performance, and a poor choice may lead to inefficiency or failure during start-up or dynamic tracking. It is then impossible to find suitable scaling factor and upper limiter of step size that satisfy the requirements of the MPPT system under enormous irradiance changes.

3) Incremental conductance method with modified variable step size (MVS1_IC) -first proposition-

The main idea of the MVS1_IC method is that the fixed line $(\Delta Dmax / N)$ of Fig. 7 should move up and down when the sun's radiation level changes between P1 and P2.

Since the change in the level of irradiation of the sun is strongly related to the output current of the PV generator [14]. Equation 7 is modified as follows:

$$Step = \frac{N}{I} \left| \frac{dP}{dV} \right| \tag{10}$$

Equation 10 shows that the |dP/dV| is compared to a variable coefficient which varies as a function of the output current of the PV module. [5], [14].

4) Incremental conductance method with modified variable step size (MVS2_IC) -second proposition-

This technique has the same principle as the MVS1_IC, but the difference is in the introduction of the dI term instead of the current value I in the equation 7 [7].

The variable step size of MVS2_IC technique is given as follows:

$$Step = N \left| \frac{dP}{dV - dI} \right| \tag{11}$$

III. SIMULATION RESULTS

To illustrate the efficiency of the four techniques of IC_MPPT method, a simulation using Psim model is realized.

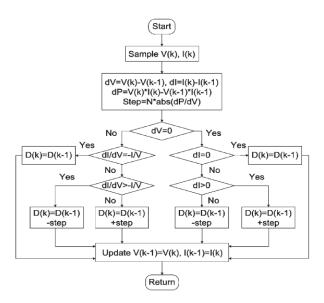


Fig. 6 Flow chart of IC method with variable step size.

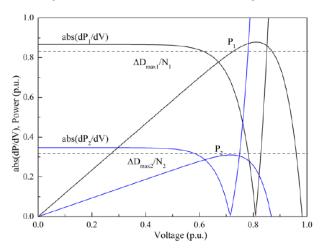


Fig. 7 Normalized power and slope of power versus voltage under different irradiation conditions.

The fixed step size is chosen to be 0,005 and the scaling factor *N* is adjusted as 0,001.

The results shown in Fig. 8, Fig. 9 and Fig. 10 are obtained for a fixed value of temperature $(25^{\circ}C)$ and a square oscillations form of solar irradiation whose switching between 800 and 1000 watts/m².

For each curves, we note that the power curve extracted by the load joins the power curve of the panel PV to finally oscillate around it. The comparison between the four techniques shows that the techniques MVS2_IC and MVS1_IC presente a high response and a good convergence speed than the FS_IC and VS_IC techniques, this is due to the fact that the duty cycle of MVS2_IC and MVS1_IC techniques change with the change of the current which varies with the variation of the Atmospheric conditions.

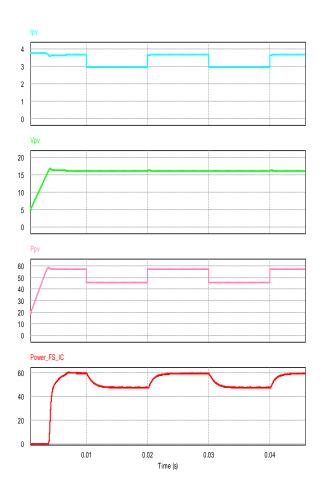


Fig. 8 Simulation results: Photovoltaic panel output current, photovoltaic panel output voltage, Photovoltaic panel output power and Photovoltaic power for DC/DC converter output

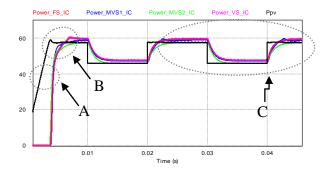
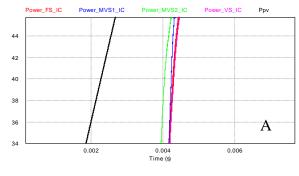
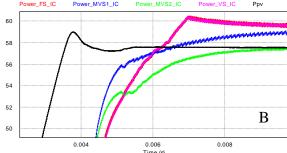


Fig. 9 Photovoltaic DC-DC converter output Power curves: FS_IC (red curve), VS_IC (magenta curve), MVS1_IC (blue curve), and MVS2_IC (green curve).

IV. CONCLUSION

This paper study four techniques of IC MPPT algorithm: FS_IC, VS_IC, MVS1_IC and MVS2_IC these techniques track the maximum power point and controls directly the extracted power from the PV.





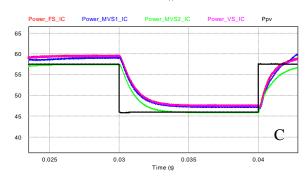


Fig. 10 Zoom in the part A, B and C of the Photovoltaic DC-DC converter output Power curves: FS_IC (red curve), VS_IC (magenta curve), MVS1_IC (blue curve), and MVS2_IC (green curve).

Compared to FS_IC and VS_IC techniques, The MVS1_IC and The MVS2_IC offer different advantages which are: good tracking efficiency, response is high and well control for the extracted power because of the adjusting of step sizes according to sun irradiation level using PV output current for increasing convergence speed and efficiency.

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